

PTO/SB/08A (10-01)

Approved for use through 10/31/2002, OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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of

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Complete if known

Application Number	10/672,961
Filing Date	September 26, 2003
First Named Inventor	Glenn J. Leedy
Art Unit	2822
Examiner Name	Monica Lewis
Attorney Docket Number	ELM/002 Cont. 4

U.S. PATENT DOCUMENTS

Examiner initials ¹	Cite No. ¹	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
ML		3,430,835	03/04/1969	Patzer et al.	
		4,196,232	04/01/1980	Schnable et al.	
		5,615,163	03/25/1997	Sakui et al.	
		5,786,629	07/28/1998	Faris	
		5,787,445	07/28/1998	Daberkow	
ML		6,563,224	05/13/2003	Leedy	

FOREIGN PATENT DOCUMENTS

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ML		JP 01-157561 A	06/20/1989	LSI Logic Corp.		
		JP 02-037655	02/07/1990	Siemens AG		
		JP 03-127816	05/30/1991	Canon KK		
		JP 03-174715	07/29/1991	Fujitsu Limited Trusit Technologies		
		JP 05-283607 A	10/29/1993	Hitachi Ltd.		
		JP 08-504060 A	04/30/1996	Irvine Sensors Corp.		
		JP 62-272556 A	11/26/1987	Fujitsu Ltd.		
		JP 61-30059 A	02/12/1986	Nippon Electro Co.		
		JP 04-042957	02/13/1992	Matsushita Electron Corp.		
		JP 05-109977	04/30/1993	Mitsubishi Electric Corp.		
		JP 09-152979 A	06/10/1997	Matsushita Electric Ind. Co. Ltd.		
		JP 10-107065	04/24/1998	IBM		
ML		JP 10-209371 A	08/08/1998	IBM		

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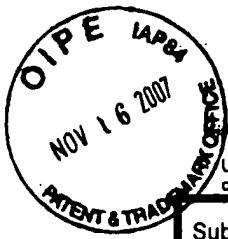
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<p>control number:</p> <p>Substitute for form 1449/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(use as many sheets as necessary)</p>				<p>Complete if known</p> <table border="1"> <tr> <td>Application Number</td> <td>10/672,961</td> </tr> <tr> <td>Filing Date</td> <td>September 26, 2003</td> </tr> <tr> <td>First Named Inventor</td> <td>Glenn J. Leedy</td> </tr> <tr> <td>Art Unit</td> <td>2822</td> </tr> <tr> <td>Examiner Name</td> <td>Monica Lewis</td> </tr> <tr> <td>Attorney Docket Number</td> <td>ELM/002 Cont. 4</td> </tr> </table>		Application Number	10/672,961	Filing Date	September 26, 2003	First Named Inventor	Glenn J. Leedy	Art Unit	2822	Examiner Name	Monica Lewis	Attorney Docket Number	ELM/002 Cont. 4
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NON PATENT LITERATURE DOCUMENTS

Examiner initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ⁴
WL		S. Wolf, Silicon Processing, 1990, Lattice Press, Volume 2, Page 111.	
MC		Wolf, Stanley and Richard N. Tauber; Silicon Procesing For the VLSI Era, Volume 1: Process Technology; Sunset Beach, CA: Lattice Press, 1986, pp. 191-194.	
WL		S. Wolf, Silicon Processing For the VLSI Era, 1990, Lattice Press, Volume 2, page 191.	

John V. Vitor



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